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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/325,882	
	Filing Date	June 4, 1999	
	First Named Inventor	Sinclair et al.	
	Group Art Unit	2182	
	Examiner Name	Harold Kim	
Total Number of Pages in This Submission	//	Attorney Docket Number	0100.9900390

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Firm or Individual name	Christopher J. Reckamp Reg. No. 34,414
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PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sinclair et al.
Serial No. 09/325,882
Filing Date: June 4, 1999
Confirmation No. 3412

Examiner: Harold Kim
Art Group: 2182
Our file no. 00100.99.0039
Docket No. 0100.9900390

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Title: **POWER REDUCTION CIRCUIT AND METHOD WITH MULTI CLOCK
BRANCH CONTROL**

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2/25/02
Date:

Rosalie Swanson
Rosalie Swanson

AMENDMENT AND RESPONSE

Dear Sir:

In response to the first office action mailed November 28, 2001, Applicants respectfully submit the following amendment and remarks.

In the claims:

Please amend Claims 1, 8 and 13 as follows. In particular, please substitute the below claims for the indicated pending claims with the same number.

1. (Once Amended) A power consumption reduction circuit comprising:

a memory clock source for a graphics controller; and

a memory clock divider circuit, operatively coupled to the memory clock source, that generates divided memory clock output signals as a plurality of corresponding independent clock signals to a number of memory interface circuits for differing processing engines and selectively